LISTING OF THE CLAIMS

This listing of claims replaces all prior versions.

1. (Previously Presented) An amplifier circuit comprising: a driver stage having first active devices which receive a signal for pre-amplification and output a pre-amplified signal; an output stage having second active devices which receive said pre-amplified signal for further amplification and output an amplified signal; a detector which measures levels of a forward signal and a reflected signal respectively of said amplified signal; and a control circuit which controls turning on and off of said first active devices and said second active devices as a function of the forward signal level and the reflected signal level to substantially maintain linearity of said amplifier circuit with load variations.

2. (Original) The amplifier circuit of claim 1, wherein said output stage is coupled to a load without an isolation device between said output stage and said load.

3. (Original) The amplifier circuit of claim 1, wherein said control circuit independently controls each of said first active devices and said second active devices.

4. (Original) The amplifier circuit of claim 1, wherein said control circuit independently controls each of said first active devices.

5. (Original) The amplifier circuit of claim 1, wherein said control circuit independently controls each of said second active devices.

6. (Original) The amplifier circuit of claim 1, wherein said first active devices and said second active devices are NPN transistors.

7. (Original) The amplifier circuit of claim 1, further comprising an input match circuit coupled between an input of said amplifier circuit and said driver stage for matching an input impedance of said amplifier circuit to an output impedance of a device coupled to said input.

8. (Original) The amplifier circuit of claim 7, further comprising at least one capacitor coupled between said input match circuit and said driver stage.

9. (Original) The amplifier circuit of claim 1, further comprising at least one capacitor coupled between an input of said amplifier circuit and said driver stage.

10. (Original) The amplifier circuit of claim 1, further comprising an inter-stage match circuit coupled between an output of said driver stage and an input of said output stage for matching an input impedance of said output stage to an output impedance of said driver stage.

11. (Original) The amplifier circuit of claim 10, further comprising at least one capacitor coupled between said inter-stage match circuit and said output stage.

12. (Previously Presented) The amplifier circuit of claim 10, further comprising at least one capacitor coupled between an output of said driver stage and said output stage.

13. (Original) A wireless communication device comprising the amplifier circuit of claim 1.

14. (Previously Presented) An amplifier circuit comprising: a driver stage having a first set of active devices which receive a signal for pre-amplification and output a pre-amplified signal; an output stage having a second set of active devices which receive said pre-amplified signal for further amplification and output an amplified signal; a detector which measures levels of a forward signal and a reflected signal respectively of said amplified signal; and a control circuit which independently and selectively controls switching each active device of said first set of active devices and said second set of active devices as a function of said levels of the forward signal level and the reflected signal level to substantially maintain linearity of said amplifier circuit with load variations.

15. (Previously Presented) A method for substantially maintaining linearity of an amplifier circuit with variations of a load coupled to an output of said amplifier circuit comprising: measuring levels of a forward signal and a reflected signal respectively at said output; and turning on and off first active devices of a driver stage of said amplifier circuit and second active devices of an output stage of said amplifier circuit as a function of the forward signal level and the reflected signal level.

16. (Original) The method of claim 15, wherein said turning act independently turns on and off each of said first active devices and said second active devices.

17. (Original) The method of claim 15, wherein said turning act independently turns on and off each of said first active devices.

18. (Original) The method of claim 15, wherein said turning act independently turns on and off each of said second active devices.

19. (Previously Presented) The amplifier circuit of claim 1, wherein the control circuit controls turning on and off of said first active devices and said second active devices as a function of the forward signal level and the reflected signal level by comparing the forward signal level to the reflected signal level.

20. (Previously Presented) The amplifier circuit of claim 1, wherein the control circuit controls turning on and off of said first active devices and said second active devices as a function of the forward signal level and the reflected signal level by comparing the forward signal level and the reflected signal level with at least one reference value.